Notice of Allowability	Application No.	Applicant(s)	
	10/638,395	NISHIDA ET AL.	
	Examiner	Art Unit	
	Gary L. Laxton	2838	
The MAILING DATE of this communication appeall claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate commul IGHTS. This application is su	this application. If not included nication will be mailed in due cou	rse. <b>THIS</b>
1. A This communication is responsive to the application filed 0	<u>08/12/03</u> .		
2. X The allowed claim(s) is/are 1-12.			
3. X The drawings filed on 12 August 2003 are accepted by the	e Examiner.		
4.  Acknowledgment is made of a claim for foreign priority unally All b)  Some* c)  None of the:  1.  Certified copies of the priority documents have 2.  Certified copies of the priority documents have 3.  Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)).  * Certified copies not received:  Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.  5.  A SUBSTITUTE OATH OR DECLARATION must be subminsFORMAL PATENT APPLICATION (PTO-152) which give (a)  including changes required by the Notice of Draftspers 1) hereto or 2)  to Paper No./Mail Date  [b)  including changes required by the attached Examiner Paper No./Mail Date  Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the stached Examiner's comment regarding REQUIREMENT	e been received.  The been received in Application ocuments have been received of this communication to file MENT of this application.  The best reason(s) why the oath or st be submitted.  The son's Patent Drawing Review of Samendment / Comment or the header according to 37 CF losit of BIOLOGICAL MATERS.	in this national stage application a reply complying with the require MINER'S AMENDMENT or NOTE declaration is deficient.  ( PTO-948) attached in the Office action of the drawings in the front (not the back 1.121(d)). ERIAL must be submitted. Note	ements
<ul> <li>Attachment(s)</li> <li>1. ☑ Notice of References Cited (PTO-892)</li> <li>2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)</li> <li>3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/Paper No./Mail Date 05/07/04</li> <li>4. ☐ Examiner's Comment Regarding Requirement for Deposit</li> </ul>	6.  Interview Su Paper No./ 08), 7.  Examiner's	formal Patent Application (PTO-15) Immary (PTO-413), Mail Date Amendment/Comment Statement of Reasons for Allowa	
of Biological Material	9. Other	Gary L. Laxton Patent Examiner Art Unit: 2838	/24/05

Application/Control Number: 10/638,395 Page 2

Art Unit: 2838

## **DETAILED ACTION**

## Allowable Subject Matter

1. Claims 1-12 are allowed.

2. The following is an examiner's statement of reasons for allowance:

Prior art fails to disclose or suggest, inter alia, a switching power supply unit comprising: a control circuit provided between one end of a feedback winding and a control terminal of a first switching element; and an output voltage detection circuit for detecting output voltage output from a rectification circuit and sending a feedback signal to the control circuit; and wherein the control circuit includes an on-period control circuit for stabilizing the output voltage by turning off the first switching element in an on-state in accordance with the feedback signal and an off-period control circuit for stabilizing the output voltage by delaying turning on of the first switching element in accordance with the feedback signal.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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## Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

US 6,577,511 Ykamaguchi et al discloses an on-period control circuit for stabilizing the output voltage by turning off the first switching element in an on-state in accordance with the feedback signal (PD1, PT1) and an off-period control circuit for stabilizing the output voltage by delaying turning on of the first switching element in accordance with the feedback winding (N3).

US 6,529,392 Nishida et al discloses an on-period control circuit for stabilizing the output voltage by turning off the first switching element in an on-state in accordance with the feedback signal (PC: PD, PT) and an off-period control circuit for stabilizing the output voltage by delaying turning on of the first switching element in accordance with the feedback winding (Nb) and delay circuit (7).

US 6,208,530 Hosotani discloses an on-period control circuit for stabilizing the output voltage by turning off the first switching element in an on-state in accordance with the feedback signal (PC: PD, PT) and an off-period control circuit for stabilizing the output voltage by delaying turning on of the first switching element in accordance with the feedback winding (Nb).

Art Unit: 2838

US 5,995,382 Miyazaki et al disclose an on-period control circuit for stabilizing the output voltage by turning off the first switching element in an on-state in accordance with the feedback signal (PC) and an off-period control circuit for stabilizing the output voltage by delaying turning on of the first switching element in accordance with the feedback winding (Lc).

Page 4

US 5,012,399 Takemura et al disclose an on-period control circuit for stabilizing the output voltage by turning off the first switching element in an on-state in accordance with the feedback signal (PC, Q3) and an off-period control circuit for stabilizing the output voltage by delaying turning on of the first switching element in accordance with the feedback winding (Nb).

Application/Control Number: 10/638,395 Page 5

Art Unit: 2838

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Gary L. Laxton whose telephone number is (571) 272-2079. The examiner can normally be reached on Monday thru Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Sherry can be reached on (571) 272-2084. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Gary L. Laxton
Patent Examiner
Art Unit 2838